

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

EXTERNAL VERIFICATION OF PACKAGE PROCESSED  
LINEWIDTHS AND SPACINGS IN SEMICONDUCTOR PACKAGES

Application Number :

Confirmation Number:

First Named Applicant: Thomas Fleischman

Attorney Docket Number: FIS920030167US1

Art Unit:

Examiner:

Search string: ( 5276963 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5276963	1994-01-11	FLANDERS			

## Signature

Examiner Name	Date